

## Rec. 656

## RECOMMENDATION 656

**INTERFACES FOR DIGITAL COMPONENT VIDEO SIGNALS  
IN 525-LINE AND 625-LINE TELEVISION SYSTEMS**

(1986)

The CCIR,

## CONSIDERING

- a. that there are clear advantages for television broadcasting organizations and programme producers in digital studio standards which have the greatest number of significant parameter values common to 525-line and 625-line systems;
- b. that a world-wide compatible digital approach will permit the development of equipment with many common features, permit operating economies and facilitate the international exchange of programmes;
- c. that to implement the above objectives, agreement has been reached on the fundamental encoding parameters of digital television for studios in the form of Recommendation 601;
- d. that the practical implementation of Recommendation 601 requires definition of details of interfaces and the data streams traversing them;
- e. that such interfaces should have a maximum of commonality between 525-line and 625-line versions;
- f. that in the practical implementation of Recommendation 601 it is desirable that interfaces be defined in both serial and parallel forms;
- g. that digital television signals produced by these interfaces may be a potential source of interference to other services, and due notice must be taken of No. 964 of the Radio Regulations,

## UNANIMOUSLY RECOMMENDS

that where interfaces are required for component-coded digital video signals in television studios, the interfaces and the data streams that will traverse them should be in accordance with the following description, defining both bit-parallel and bit-serial implementations.

**1. Introduction**

This Recommendation describes the means of interconnecting digital television equipment operating on the 525-line or 625-line standards and complying with the 4 : 2 : 2 encoding parameters as defined in Recommendation 601.

Part I describes the signal format common to both interfaces.

Part II describes the particular characteristics of the bit-parallel interface.

Part III describes the particular characteristics of the bit-serial interface.

## PART I

## COMMON SIGNAL FORMAT OF THE INTERFACES

**1. General description of the interfaces**

The interfaces provide a unidirectional interconnection between a single source and a single destination.

A signal format common to both parallel and serial interfaces is described in § 2 below.

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The data signals are in the form of binary information coded in 8-bit words. These signals are:

- video data;
- timing reference codes;
- ancillary data;
- identification codes.

## 2. Video data

## 2.1 Coding characteristics

The video data is in compliance with Recommendation 601, and with the field-blanking definition shown in Table 1.

TABLE I — Field interval definitions

		625	525
V-digital field blanking			
Field 1	Start (V = 1)	Line 624	Line 1
	Finish (V = 0)	Line 23	Line 10
Field 2	Start (V = 1)	Line 311	Line 264
	Finish (V = 0)	Line 336	Line 273
F-digital field identification			
Field 1	F = 0	Line 1	Line 4
Field 2	F = 1	Line 313	Line 266

*Note 1* — Signals F and V change state synchronously with the end of active video timing reference code at the beginning of the digital line.

*Note 2* — Definition of line numbers is to be found in Report 624. Note that digital line number changes state prior to 0<sub>H</sub> as shown in Fig. 1.

## 2.2 Video data format

The data words 0 and 255 (00 and FF in hexadecimal notation) are reserved for data identification purposes and consequently only 254 of the possible 256 words may be used to express a signal value.

The video data words are conveyed as a 27 Mwords/s multiplex in the following order:

$C_B, Y, C_R, Y, C_B, Y, C_R$ , etc.

where the word sequence  $C_B, Y, C_R$ , refers to co-sited luminance and colour-difference samples and the following word, Y, corresponds to the next luminance sample.

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2.3 Timing relationship between video data and the analogue synchronizing waveform

2.3.1 Line interval

The digital active line begins at 244 words (in the 525-line standard) or at 264 words (in the 625-line standard) after the leading edge of the analogue line synchronization pulse, this time being specified between half-amplitude points.

Figure 1 shows the timing relationship between video and the analogue line synchronization.

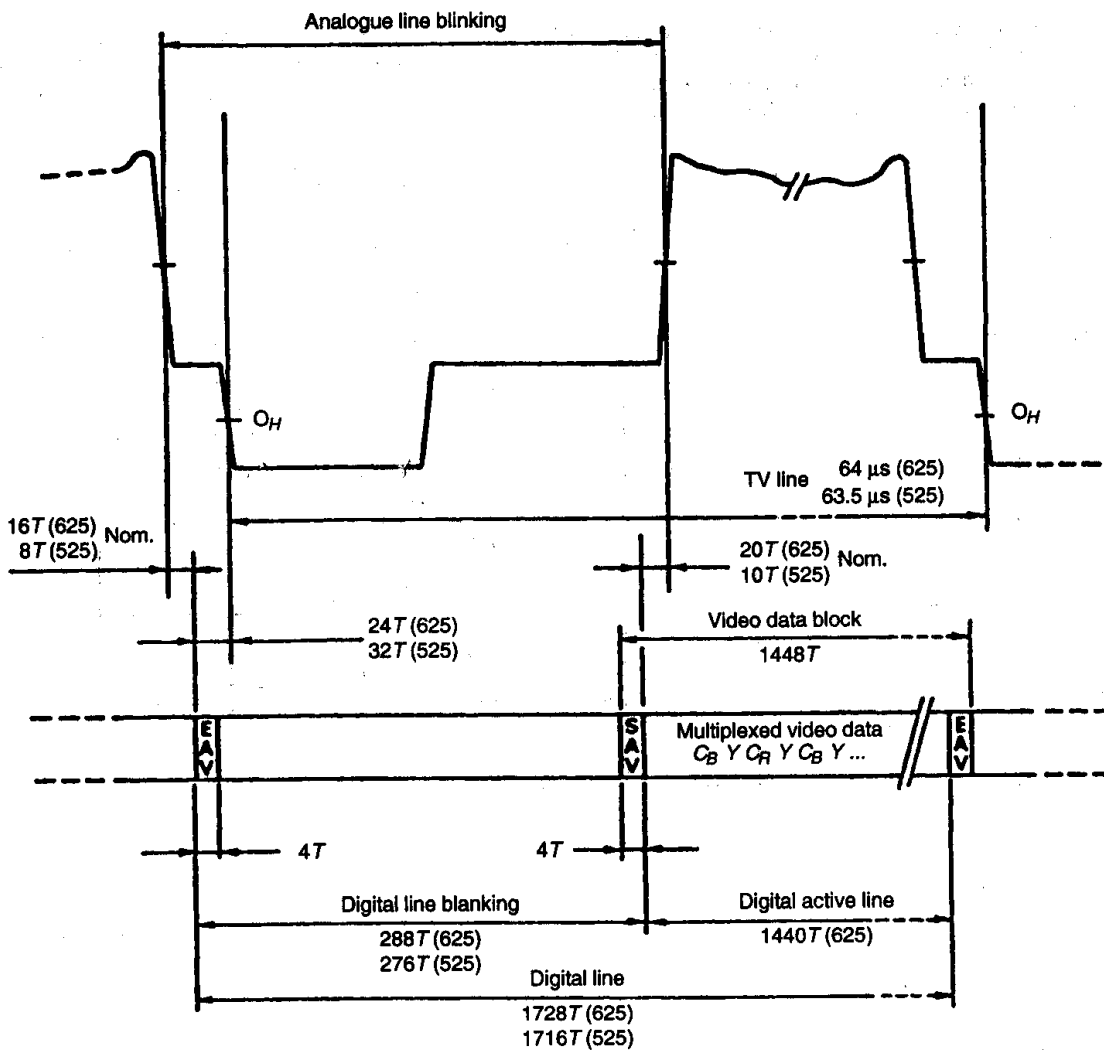


FIGURE 1 – Data format and timing relationship with the analogue video signal

T: clock period 37 ns nom.  
 SAV: start of active video timing reference code  
 EAV: end of active video timing reference code

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## 2.3.2 Field interval

The start of the digital field is fixed by the position specified for the start of the digital line: the digital field starts 32 words (in the 525-line systems) and 24 words (in the 625-line systems) prior to the lines indicated in Table I.

## 2.4 Video timing reference codes (SAV, EAV)

There are two timing reference codes, one at the beginning of each video data block (Start of Active Video, SAV) and one at the end of each video data block (End of Active Video, EAV) as shown in Fig. 1.

Each timing reference code consists of a four word sequence in the following format: FF 00 00 XY. (Values are expressed in hexadecimal notation. Codes FF, 00 are reserved for use in timing reference codes.) The first three words are a fixed preamble. The fourth word contains information defining field 2 identification, the state of field blanking, and the state of line blanking. The assignment of bits within the timing reference code is shown below in Table II.

TABLE II — Video timing reference codes

Word	Bit No.							
	7 (MSB)	6	5	4	3	2	1	0 (MSB)
First	1	1	1	1	1	1	1	1
Second	0	0	0	0	0	0	0	0
Third	0	0	0	0	0	0	0	0
Fourth	1	F	V	H	P <sub>3</sub>	P <sub>2</sub>	P <sub>1</sub>	P <sub>0</sub>

F = 0 during field 1  
1 during field 2

V = 0 elsewhere  
1 during field blanking

H = 0 in SAV  
1 in EAV

P<sub>0</sub>, P<sub>1</sub>, P<sub>2</sub>, P<sub>3</sub> : protection bits (see Table III).

MSB: most significant bit

LSB: least significant bit

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Table I defines the state of the V and F bits.

Bits  $P_0$ ,  $P_1$ ,  $P_2$ ,  $P_3$ , have states dependent on the states of the bits F, V and H as shown in Table III. At the receiver this arrangement permits one-bit errors to be corrected and two-bit errors to be detected.

TABLE III — Protection bits

Bit No.	7	6	5	4	3	2	1	0
Function	Fixed 1	F	V	H	$P_3$	$P_2$	$P_1$	$P_0$
0	1	0	0	0	0	0	0	0
1	1	0	0	1	1	1	0	1
2	1	0	1	0	1	0	1	1
3	1	0	1	1	0	1	1	0
4	1	1	0	0	0	1	1	1
5	1	1	0	1	1	0	1	0
6	1	1	1	0	1	1	0	0
7	1	1	1	1	0	0	0	1

## 2.5 Ancillary data

Provision is made for ancillary data to be inserted synchronously into the multiplex during the blanking intervals at a rate of 27 Mwords/s. Such data is conveyed by one or more 7-bit words, each with an additional parity bit (LSB) giving odd parity.

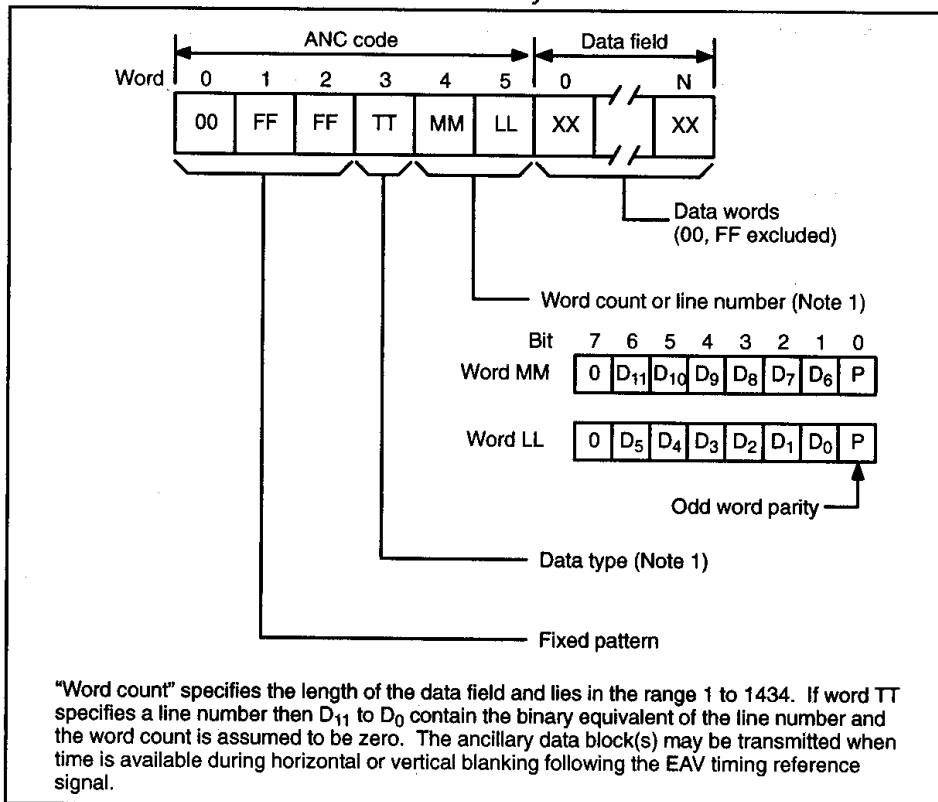
Each ancillary data block, when used, should be constructed as shown in Table IV from the timing reference code ANC and a data field.

## 2.6 Data words during blanking

The data words occurring during digital blanking intervals that are not used for the timing reference code ANC or for ancillary data are filled with the sequence 80, 10, 80, 10, etc. (values are expressed in hexadecimal notation) corresponding to the blanking level of the  $C_B$ , Y,  $C_R$ , Y signals respectively, appropriately placed in the multiplexed data.

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TABLE IV — Ancillary data block



Note 1 — The precise location of the ancillary data blocks and the coding of words 3, 4 and 5 require further study.

PART II

BIT-PARALLEL INTERFACE

1. General description of the interface

The bits of the digital code words that describe the video signal are transmitted in parallel by means of eight conductor pairs, where each carries a multiplexed stream of bits (of the same significance) of each of the component signals, C<sub>B</sub>, Y, C<sub>R</sub>, Y. The eight pairs also carry ancillary data that is time-multiplexed into the data stream during video blanking intervals. A ninth pair provides a synchronous clock at 27MHz.

The signals on the interface are transmitted using balanced conductor pairs. Cable lengths of up to 50 m (≅ 160 feet) without equalization and up to 200 m (≅ 650 feet) with appropriate equalization (see § 6) may be employed.

The interconnection employs a twenty-five pin D-subminiature connector equipped with a locking mechanism (see § 5).

For convenience, the eight bits of the data word are assigned the names DATA 0 to DATA 7. The entire word is designated as DATA (0-7). DATA 7 is the most significant bit.

Video data is transmitted in NRZ form in real time (unbuffered) in blocks, each comprising one active television line.

2. Data signal format

The interface carries data in the form of 8 parallel data bits and a separate synchronous clock. Data is coded in NRZ form. The recommended data format is described in Part I.

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**3. Clock signal****3.1 General**

The clock signal is a 27 MHz square wave where the 0-1 transition represents the data transfer time. This signal has the following characteristics:

Width:  $18.5 \pm 3$  ns

Jitter: Less than 3 ns from the average period over one field.

**3.2 Clock-to-data timing relationship**

The positive transition of the clock signal shall occur midway between data transitions as shown in Fig. 2.

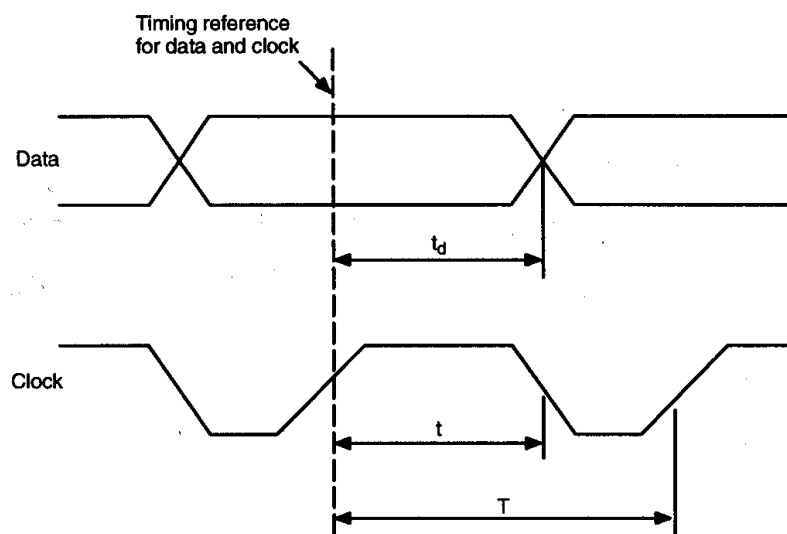


FIGURE 2 — Clock-to-data timing (at source)

Clock period (625):  $T = \frac{1}{1728 f_H} = 37 \text{ ns}$

Clock period (525):  $T = \frac{1}{1716 f_H} = 37 \text{ ns}$

Clock pulse width:  $t = 18.5 \pm 3 \text{ ns}$

Data timing – sending end:  $t_d = 18.5 \pm 3 \text{ ns}$

$f_H$ : line frequency

**4. Electrical characteristics of the interface****4.1 General**

The interface employs nine line drivers and nine line receivers.

Each line driver (source) has a balanced output and the corresponding line receiver (destination) a balanced input (see Fig. 3).

Although the use of ECL technology is not specified, the line driver and receiver must be ECL-compatible, i.e. they must permit the use of ECL for either drivers or receivers.

All digital signal time intervals are measured between the half-amplitude points.

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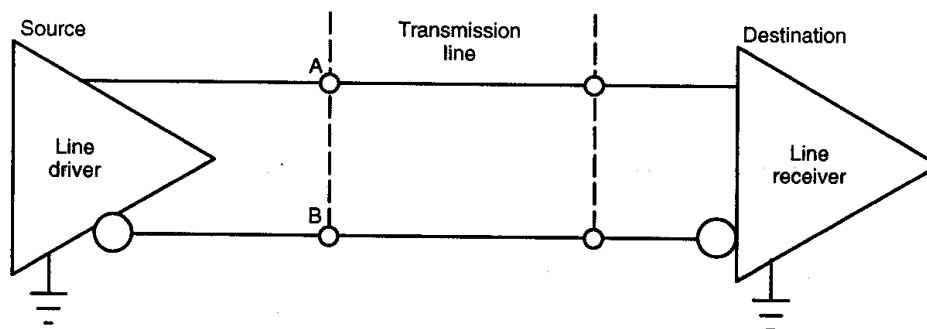


FIGURE 3 — Line driver and line receiver interconnection

## 4.2 Logic convention

The A terminal of the line driver is positive with respect to the B terminal for a binary 1 and a negative for a binary 0 (see Fig. 3).

## 4.3 Line driver characteristics (source)

4.3.1 *Output impedance:* 110  $\Omega$  maximum

4.3.2 *Common mode voltage:*  $-1.29 \text{ V} \pm 15\%$  (both terminals relative to ground).

4.3.3 *Signal amplitude:* 0.8 to 2.0 V peak-to-peak, measured across a 110  $\Omega$  resistive load.

4.3.4 *Rise and fall times:* less than 5 ns, measured between the 30% and 80% amplitude points, with a 110  $\Omega$  resistive load. The difference between rise and fall times must not exceed 2 ns.

## 4.4 Line receiver characteristics

4.4.1 *Input impedance:* 110  $\Omega \pm 10 \Omega$ .

4.4.2 *Maximum input signal:* 2.0 V peak-to-peak.

4.4.3 *Minimum input signal:* 185 mV peak-to-peak.

However, the line receiver must sense correctly the binary data when a random data signal produces the conditions represented by the eye diagram in Fig. 4 at the data detection point.

4.4.4 *Maximum common mode signal:*  $\pm 0.5 \text{ V}$ , comprising interference in the range 0 to 15 kHz (both terminals to ground).

4.4.5 *Differential delay:* Data must be correctly sensed when the clock-to-data differential delay is in the range between  $\pm 11 \text{ ns}$  (see Fig. 4).

## 5. Mechanical details of the connector

The interface uses the 25 contact type D subminiature connector specified in ISO Document 2110-1980, with contact assignment shown in Table V.

Connectors are locked together by a one-piece slide lock on the cable connectors and locking posts on the equipment connectors. Connectors employ pin contacts and equipment connectors employ socket contacts. Shielding of the interconnecting cable and its connectors must be employed (see Note).

*Note* — It should be noted that the ninth and eighteenth harmonics of the 13.5 MHz sampling frequency (nominal value) specified in Recommendation 601 fall at the 121.5 and 243 MHz aeronautical emergency channels. Appropriate precautions must therefore be taken in the design and operation of interfaces to ensure that no interference is caused at these frequencies. Emission levels for related equipment are given in CISPR Recommendation: "Information technology equipment – limits of interference and measuring methods" Document CISPR/B (Central Office) 16. Nevertheless, No. 964 of the Radio Regulations prohibits any harmful interference on the emergency frequencies.



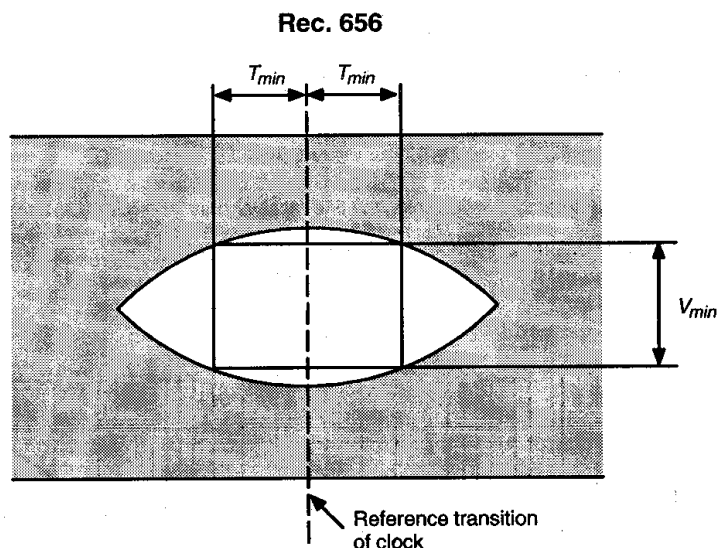


FIGURE 4 — Idealized eye diagram corresponding to the minimum input signal level

$$T_{min} = 11 \text{ ns}$$

$$V_{min} = 100 \text{ mV}$$

Note — The width of the window in the eye diagram, within which data must be correctly detected comprises  $\pm 3$  ns clock jitter,  $\pm 3$  ns data timing (see § 3.2), and  $\pm 5$  ns available for differences in delay between pairs of the cable.

TABLE V — Contact assignments

Contact	Signal line	Contact	Signal line
1	Clock A	14	Clock B
2	System ground	15	System ground
3	Data 7A (MSB)	16	Data 7B
4	Data 6A	17	Data 6B
5	Data 5A	18	Data 5B
6	Data 4A	19	Data 4B
7	Data 3A	20	Data 3B
8	Data 2A	21	Data 2B
9	Data 1A	22	Data 1B
10	Data 0A	23	Data 0B
11	Spare A-A	24	Spare A-B
12	Spare B-A	25	Spare B-B
13	Cable shield	—	—

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Any spare pairs connected to contacts 11,24 or 12,25 are reserved for bits of lower significance than those carried on contacts 10,23.

## 6. Line receiver equalization

To permit correct operation with longer interconnection links, the line receiver may incorporate equalization.

When equalization is used, it should conform to the nominal characteristics of Fig. 5. This characteristic permits operation with a range of cable lengths down to zero. The line receiver must satisfy the maximum input signal condition of § 4.4

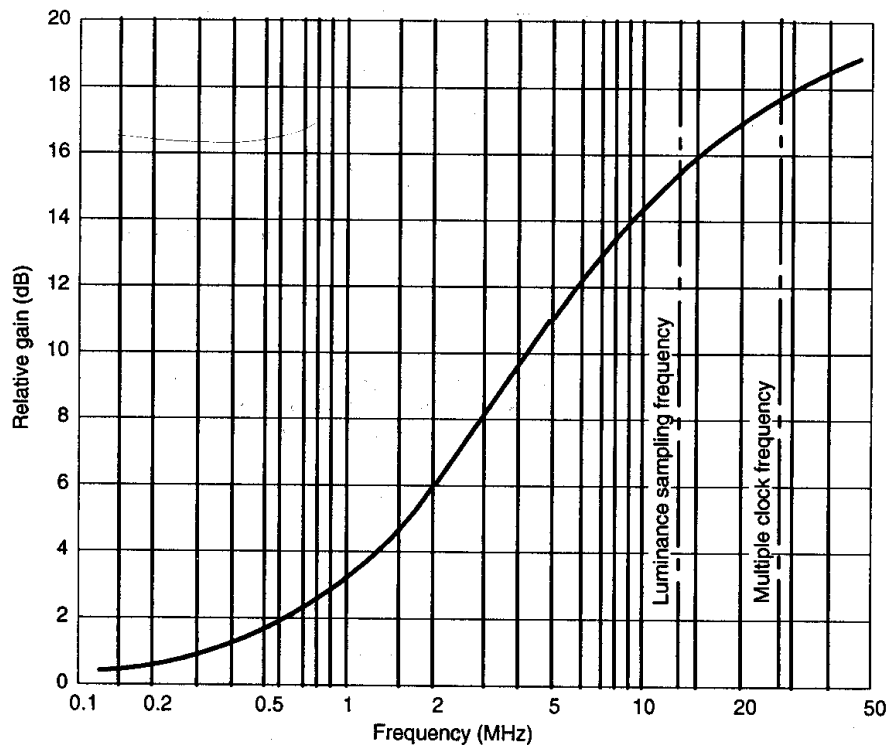


FIGURE 5 — Line receiver equalization characteristic for small signals

### PART III

#### BIT-SERIAL INTERFACE

##### 1. General description of the interface

The multiplexed data stream of 8-bit words (as described in Part I) is transmitted over a single channel in bit-serial form. Prior to transmission, additional coding takes place to provide spectral shaping, word synchronization and to facilitate clock recovery.

##### 2. Coding

The 8-bit data words are encoded for transmission into 9-bit words as shown in Table VI.

For some 8-bit data words alternative 9-bit transmission words exist, as shown in columns 9B and  $\bar{9}B$ , each 9-bit word being the complement of the other. In such cases, the 9-bit word will be selected alternately from columns 9B and  $\bar{9}B$  on each successive occasion that any such 8-bit word is conveyed. In the decoder, either word must be converted to the corresponding 8-bit data word.

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TABLE VI — Encoding table

Input	Output		Input	Output		Input	Output		Input	Output		Input	Output		Input	Output	
8B	9B	9B̄	8B	9B	9B̄	8B	9B	9B̄	8B	9B	9B̄	8B	9B	9B̄	8B	9B̄	9B̄
00	0FE	101	2B	053		56	097		81	0AA		AC	12C		D7	0CC	
01	027		2C	1AC		57	168		82	055		AD	0D9		D8	139	
02	1D8		2D	057		58	099		83	1AA		AE	126		D9	0CE	
03	033		2E	1A8		59	166		84	0D5		AF	0E5		DA	133	
04	1CC		2F	059		5A	09B		85	12A		B0	11A		DB	0D8	
05	037		30	1A6		5B	164		86	095		B1	0E9		DC	131	
06	1CB		31	05B		5C	09D		87	16A		B2	116		DD	0DC	
07	039		32	05D		5D	162		88	0B5		B3	02E		DE	127	
08	1C6		33	1A4		5E	0A3		89	14A		B4	1D1		DF	0E2	
09	03B		34	065		5F	15C		8A	09A		B5	036		E0	123	
0A	1C4		35	19A		60	0A7		8B	165		B6	1C9		E1	0E4	
0B	03D		36	069		61	158		8C	0A6		B7	03A		E2	11D	
0C	1C2		37	196		62	025	1DA	8D	159		B8	1C5		E3	0E6	
0D	14D		38	026	1D9	63	0A1	15E	8E	0AC		B9	04E		E4	11B	
0E	0B4		39	08C	173	64	029	1D6	8F	153		BA	1B1		E5	0E8	
0F	14B		3A	02C	1D3	65	091	16E	90	0AE		BB	05C		E6	119	
10	1A2		3B	098	167	66	045	1BA	91	151		BC	1A3		E7	0EC	
11	0B6		3C	032	1CD	67	089	176	92	02A	1D5	BD	05E		E8	117	
12	149		3D	0BE	141	68	049	1B6	93	092	16D	BE	1A1		E9	0F2	
13	0BA		3E	034	1CB	69	085	17A	94	04A	1B5	BF	066		EA	113	
14	145		3F	0C2	13D	6A	051	1AE	95	094	16B	C0	199		EB	0F4	
15	0CA		40	046	1B9	6B	08A	175	96	0A8	157	C1	06C		EC	10D	
16	135		41	0C4	13B	6C	0A4	15B	97	0B7	148	C2	193		ED	076	
17	0D2		42	04C	1B3	6D	054	1AB	98	0F5	10A	C3	06E		EE	10B	
18	12D		43	0C8	137	6E	0A2	15D	99	0BB	144	C4	191		EF	0C7	
19	0D4		44	058	1A7	6F	052	1AD	9A	0ED	112	C5	072		F0	13C	
1A	129		45	0B1		70	056		9B	0BD	142	C6	18D		F1	047	
1B	0D6		46	14E		71	1A9		9C	0EB	114	C7	074		F2	1B8	
1C	125		47	0B3		72	05A		9D	0D7	128	C8	18B		F3	067	
1D	0DA		48	14C		73	1A5		9E	0DD	122	C9	07A		F4	19C	
1E	115		49	0B9		74	06A		9F	0DB	124	CA	189		F5	071	
1F	0EA		4A	06B		75	195		A0	146		CB	08E		F6	198	
20	0B2		4B	194		76	096		A1	0C5		CC	185		F7	073	
21	02B		4C	06D		77	169		A2	13A		CD	09C		F8	18E	
22	1D4		4D	192		78	0A9		A3	0C9		CE	171		F9	079	
23	02D		4E	075		79	156		A4	136		CF	09E		FA	18C	
24	1D2		4F	18A		7A	0AB		A5	0CB		D0	163		FB	087	
25	035		50	08B		7B	154		A6	134		D1	0B8		FC	186	
26	1CA		51	174		7C	0A5		A7	0CD		D2	161		FD	0C3	
27	04B		52	08D		7D	15A		A8	132		D3	0BC		FE	178	
28	1B4		53	172		7E	0AD		A9	0D1		D4	147		FF	062	19D
29	04D		54	093		7F	152		AA	12E		D5	0C6				
2A	1B2		55	16C		80	155		AB	0D3		D6	143				

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**3. Order of transmission**

The least significant bit of each 9-bit word shall be transmitted first.

**4. Logic convention**

The signal is conveyed in NRZ form. The voltage at the output terminal of the line driver shall increase on a transition from 0 to 1 (positive logic).

**5. Transmission medium**

The bit-serial data stream can be conveyed using either a coaxial cable (§ 6) or fibre optic bearer (§ 7).

**6. Characteristics of the electrical interface****6.1 Line driver characteristics (source)****6.1.1 Output impedance**

The line driver has an unbalanced output with a source impedance of 75  $\Omega$  and a return loss of at least 15 dB over a frequency range of 10 to 243 MHz.

**6.1.2 Signal impedance**

The peak-to-peak signal amplitude lies between 400 mV and 700 mV measured across a 75  $\Omega$  resistive load directly connected to the output terminals without any transmission line.

**6.1.3 DC offset**

The DC offset with reference to the mid amplitude point of the signal lies between +1.0V and -1.0 V.

**6.1.4 Rise and fall times**

The rise and fall times, determined between the 20% and 80% amplitude points and measured across a 75  $\Omega$  resistive load connected directly to the output terminals, shall lie between 0.75 and 1.5 ns and shall not differ by more than 0.40 ns.

**6.1.5 Jitter**

The timing of the rising edges of the data signal shall be within  $\pm 0.10$  ns of the average timing of rising edges, as determined over a period of one line.

**6.2 Line receiver characteristics (destination)****6.2.1 Terminating impedance**

The cable is terminated by 75  $\Omega$  with a return loss of at least 15 dB over a frequency range of 10 to 243 MHz.

**6.2.2 Receiver sensitivity**

The line receiver must sense correctly random binary data either when connected directly to a line driver operating at the extreme voltage limits permitted by § 6.1.2, or when connected via a cable having loss of 40 dB at 243 MHz and a loss characteristic of  $1/\sqrt{f}$ .

Over the range 0 to 12 dB no equalization adjustment is required; beyond this range adjustment is permitted.

**6.2.3 Interference rejection**

When connected directly to a line driver operating at the lower limit specified in § 6.1.2, the line receiver must correctly sense the binary data in the presence of a superimposed interfering signal at the following levels:

d.c.	$\pm 2.5$ V
Below 1 kHz:	2.5 V peak-to-peak
1 kHz to 5 MHz:	100 mV peak-to-peak
Above 5 MHz:	40 mV peak-to-peak

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**6.3 Cables and connectors****6.3.1 Cable**

It is recommended that the cable chosen should meet any relevant national standards on electro-magnetic radiation.

*Note* — It should be noted that the ninth and eighteenth harmonics of the 13.5 MHz sampling frequency (nominal value) specified in Recommendation 601 fall at the 121.5 and 243 MHz aeronautical emergency channels. Appropriate precautions must therefore be taken in the design and operation of interfaces to ensure that no interference is caused at these frequencies. Emission levels for related equipment are given in CISPR Recommendation: "Information technology equipment – limits of interference and measuring methods" (Document CISPR/B (Central Office) 16). Nevertheless, No. 964 of the Radio Regulations prohibits any harmful interference on the emergency frequencies.

**6.3.2 Characteristic impedance**

The cable used shall have a nominal characteristic impedance of 75  $\Omega$ .

**6.3.3 Connector characteristics**

The connector shall have mechanical characteristics conforming to the standard BNC type (IEC Publication 169-8), and its electrical characteristics should permit it to be used at frequencies up to 500 MHz in 75  $\Omega$  circuits.

**7. Characteristics**

To be defined.

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